IN THE SPECIFICATION

Please amend the Abstract of the Disclosure as follows:

~~The invention concerns a~~ [[t]]Technology for detecting a frame synchronous pattern

~~comprising:~~ that includes a data switch section for performing [[a]] data rearrangement

processing of parallel data having a given frame synchronous pattern; a temporary region

detection section for temporarily detecting a candidate of region data containing the frame

synchronous pattern from the parallel data; a frame synchronous pattern detection section for

detecting the frame synchronous pattern from the temporary region data of the temporary region

detection section; and a data switch control section for controlling data rearrangement processing

by the data switch section according to the detection state of the temporary region data by the

temporary region detection section and to the detection state of the frame synchronous pattern by

the frame synchronous pattern detection section, in order to detect precisely the frame

synchronous pattern in m parallel data without enabling the detection of m ways of frame

synchronous patterns in m parallel data.

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